

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Daniel A. Day
Title: Selective Control of Test-Access Ports in Integrated Circuits

Docket No.: 884.879US1
Filed: June 30, 2003
Examiner: Phung M. Chung

Serial No.: 10/612,293
Due Date: April 19, 2008
Group Art Unit: 2117

MS Issue Fee


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

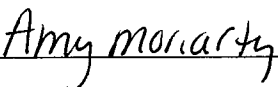
X Examiner Interview Summary (1 pg.).

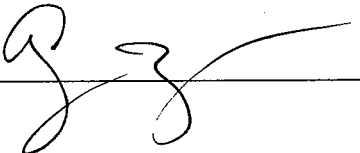
Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.
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By: 
Atty: Robert B. Madden
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 26th day of March, 2008.


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(GENERAL)